

ABSTRACT

A reception apparatus capable of calibrating a DC offset voltage fast and with high accuracy even in an environment in which interferer exist without causing noise characteristic degradation. In this apparatus, a digital signal processing section (108) controls the gain of a received signal at such a gain that predetermined reception quality is obtained. A time constant control circuit (110) controls the time constant and makes the amount of attenuation of the received signal of a low pass filter (106a) more moderate compared to the case where a DC offset voltage is not calibrated during DC offset voltage calibration. A voltage calibration circuit (111) calibrates the DC offset voltage generated in the received signal when controlling the gain. A second decoder (112) compares the gain during gain control with a threshold and instructs an operation control circuit (113) to set a high-frequency circuit (114) in a non-operating state when the gain during gain control is equal to or above the threshold and set the high-frequency circuit (114) in an operating state when the gain during gain control is less than the threshold.